**B**3

-- Figs. 4A and 4B are flow diagrams showing a preferred implementation of an intelligent port distribution algorithm according to the invention. --

On page 14, beginning at line 24, delete the text and replace with the following:

BH

-- Figs. 4A and 4B are flow diagrams showing a preferred implementation of an intelligent port distribution algorithm according to the invention. The following pseudo code listing explains the algorithm in connection with Fig. 4A and Fig. 4B. --

## **REMARKS**

The specification has been amended to properly designate this application as a divisional of application Serial No. 08/738,940, filed October 24, 1996, now allowed.

Additionally, the specification was amended to conform with the new formal drawings filed herewith.

If there is any fee due in connection with the filing of this Preliminary Amendment, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: January 8, 2002

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PATENT Customer No. 22,852 Attorney Docket No. 07275-0030-01

## APPENDIX TO PRELIMINARY AMENDMENT

VERSION WITH MARKINGS TO SHOW CHANGES MADE

## **AMENDMENTS TO THE SPECIFICATION**

On page 1, line 5 the following heading is inserted:

-- CROSS REFERENCE TO RELATED APPLICATION --

On page 1, line 6 the following new paragraph is inserted:

-- This application is a divisional of application Serial No. 08/738,940, filed

October 24, 1996 now allowed, which is incorporated herein by reference. --

On page 7, beginning at line 19, the text is amended as follows:

[Fig. 4 is a flow diagram] Figs. 4A and 4B are flow diagrams showing a preferred implementation of an intelligent port distribution algorithm according to the invention.

On page 14, beginning at line 24, the text is amended as follows:

[Fig. 4 is a flow diagram] Figs. 4A and 4B are flow diagrams showing a preferred implementation of an intelligent port distribution algorithm according to the invention.

The following pseudo code listing explains the algorithm in connection with [Fig. 4]

Fig. 4A and Fig. 4B.

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